



# Delta-Sigma Data Converters: Theory, Design, and Simulation

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This comprehensive guide offers a detailed treatment of the analysis, design, simulation and testing of the full range of today's leading delta-sigma data converters. Written by professionals experienced in all practical aspects of delta-sigma modulator design, *Delta-Sigma Data Converters* provides comprehensive coverage of low and high-order single-bit, bandpass, continuous-time, multi-stage modulators as well as advanced topics, including idle-channel tones, stability, decimation and interpolation filter design, and simulation.

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### Editorial Review

From the Back Cover

Delta-Sigma modulators are among the key components in digital audio and multimedia systems. This comprehensive guide offers a detailed treatment of the analysis, design, simulation, and testing of the full range of today's leading delta-sigma data converters. Written by professionals experienced in all practical aspects of delta-sigma modulator design, DELTA-SIGMA DATA CONVERTERS provides comprehensive coverage of low- and high-order single-bit and multibit, low-pass and bandpass, sampled-data and continuous-time, multistage modulators as well as advanced topics, including idle-channel tones, stability, decimation and interpolation filter design, and simulation. Features include:

- A detailed introductory tutorial
- Up-to-date research results
- Expert guidance on practical aspects
- Lots of hands-on advice!

With DELTA-SIGMA DATA CONVERTERS, you will be able to design cheaper, more reliable, and higher-performance data converters. This book will be of great value as a graduate or continuing-education textbook as well as supplemental reading for engineers who want to keep pace with this fast-developing technology.

About the Author

**Steven R. Nosworthy** is currently worldwide Operations Manager for the Wireless, Telecom, and Video Business Units of Motorola Semiconductor (Analog Division) in Tempe, Arizona. He has published numerous papers and holds 18 patents in the area of delta-sigma converters. In addition to having taught courses in digital processing at Pennsylvania State University, Mr. Nosworthy holds two degrees in music, and has performed in numerous professional symphony orchestras.

**Richard Schreier** is Associate Professor of Electrical Engineering at Oregon State University. From 1986 to 1988 he worked as a Member of the Scientific Staff in the CMOS cell library group of Bell-Northern Research in Ottawa, Canada.

**Gabor C. Temes** is Professor of Electrical Engineering at Oregon State University. He has written approximately 250 publications and is author of several books. A Fellow of the IEEE, DR. Temes has received numerous IEEE awards, including the Centennial Medal, the Technical Achievement Award, and the IEEE Circuits and Systems Society Education Award.

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